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CENTRAL FAX CENTER**SEP 08 2006****REMARKS**

The present amendment is submitted in response to the Office Action dated July 6, 2006, which set a three-month period for response, making this amendment due by October 6, 2006.

Claims 1-14 are pending in this application.

In the Office Action, the abstract of the disclosure and claim 13 were objected to for informalities. Claim 13 was rejected under 35 U.S.C. 112, second paragraph, as being indefinite. Claim 12 was rejected under 35 U.S.C. 101 on grounds the claim invention is directed to non-statutory subject matter. Claims 1-5 and 11-13 were rejected under 35 U.S.C. 102(b) as being anticipated by U.S. Patent No. 5,172,115 to Kerth.

The Applicants note with appreciation the allowance of claims 6-10, if rewritten in independent form to include the limitations of the base claim and any intervening claims.

In the present amendment, the specification has been amended to add standard headings. The abstract was amended to combine the two paragraphs into a single paragraph.

Claims 12 and 13 were amended to address the objection and rejection under 35 U.S.C. 112, second paragraph. New claim 14 was added, which recites the narrower limitation that was eliminated from claim 12.

With regard to the substantive rejection of claims 1-5 and 11-13, the Applicants respectfully disagree that the Kerth reference discloses all of the features of these claims.

Kerth shows a ratiometric converter, in which the input voltage is compared with an internal reference. During a calibration mode, non-ratiometric offset voltages of the switch are eliminated, such that a short circuit of the reference voltages occurs by grounding of switches.

In Kerth, the non-ratiometric offset voltages of the switch are removed concretely, such that a short circuit of the reference voltages occurs by grounding switches 72, 73. The voltages act for the removal of interferences in the reference voltages. The supply voltage of the charged (Fig. 3, charge cell 10) and the supply voltage of the ratiometric converter are generally not considered in Kerth.

In contrast, the present invention relates to a compensation device of two supply voltages and their deviations. One of the supply voltages (for example, 5V+/-2%) supplies a sensor. The other supply voltage (for example, 3.3V+/-3%) supplies an analog digital converter. With the present invention, the deviations of +/-2% and +/-3% of the supply voltages are compensated.

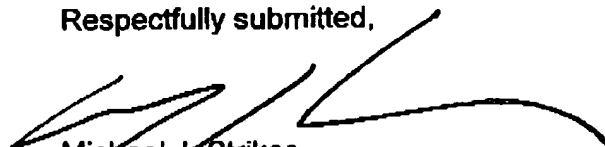
Because Kerth fails to disclose these features, the rejection of claims 1-5 and 11-13 under Section 102 must be withdrawn. A prior art reference anticipates a claim only if the reference discloses every limitation of the claim. Absence from the reference of any claimed element negates anticipation. *Row v. Dror*, 42 USPQ 2d 1550, 1553 (Fed. Cir. 1997).

For the reasons set forth above, the Applicants respectfully submit that claims 1-5 and 11-14 also are patentable over the cited art. The Applicants further request withdrawal of the rejection under 35 U.S.C. 102 and reconsideration of the claims as herein amended.

In light of the foregoing amendments and arguments in support of patentability, the Applicants respectfully submit that this application stands in condition for allowance. Action to this end is courteously solicited.

Should the Examiner have any further comments or suggestions, the undersigned would very much welcome a telephone call in order to discuss appropriate claim language that will place the application into condition for allowance.

Respectfully submitted,



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